

IN THE SPECIFICATION

Please insert new paragraph [0012a] as follows:

[0012a] U. S. Patent No. 5,533,064 discloses a digital radio receiver. An amplifier limiter receives an input and uses a plurality of limiting amplifiers to produce a constant amplitude signal. Using taps between the plurality of amplifiers, detectors are used to determine a log value of the input. A low pass filter removes harmonics from the constant amplitude signal. An orthogonal detector produces I and Q components of the constant amplitude signal. The I and Q components are converted to digital values. The log value is delayed and converted to a digital value. Using a log to linear converter the log value is converted into a digital value which is multiplied with the I and Q digital values respectively.